Appl. No. 10/787,023 Amdt. Dated Feb. 3,2005

Reply to Office Action of November 3, 2004

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the

application:

Listing of Claims

Claim 1 (currently amended): A method for electrically interconnecting two

printed circuit boards, comprising the steps of:

providing a first printed circuit board and a second printed circuit board, a

receiving slot being formed in one of the first and the second printed circuit board;

orthogonally intersecting the first and the second printed circuit boards with

each other; and

providing at least one electrical connector adjacent the receiving slot and in

electrical connection with the first and the second printed circuit boards, said at

least one electrical connector having an insulative housing and an electrical contact

received therein.

Claim 2 (original): The method as recited in claim 1, wherein the first and the

second printed circuit boards together define four quadrants.

Claim 3 (original): The method as recited in claim 2, wherein the at least one

electrical connector comprises a first connector arranged in a first quadrant and a

second connector arranged in a fourth quadrant.

2

PAGE 3/13 * RCVD AT 2/3/2005 11:21:48 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID:408 919 8353 * DURATION (mm-ss):04-08

P.04

Appl. No. 10/787,023 Amdt. Dated Feb. 3,2005 Reply to Office Action of November 3, 2004

FEB-03-2005 20:37

Claim 4 (original): The method as recited in claim 3, wherein the first and the second connectors are mounted on the second printed circuit board.

Claim 5 (original) The method as recited in claim 4, wherein the first and the second electrical connectors each comprise an electrical contact and an actuator capable of actuating the contact to have a wiping contact with the first and the second printed circuit boards.

Claim 6 (currently amended): A method for electrically interconnecting multiple printed circuit boards, comprising the steps of:

providing a plurality of first printed circuit boards;

providing a plurality of second printed circuit boards;

providing receiving slots in either the first printed circuit boards or the second printed circuit boards such that the first and the second printed circuit boards are orthogonally intersected with each other, every two orthogonally arranged printed circuit boards together defining four quadrants; and

providing in at least one of the four quadrants of every two orthogonally arranged printed circuit boards, a respective electrical connector <u>having an insulative housing and a plurality of contacts received therein</u> to electrically interconnect the first and the second printed circuit boards.

Claim 7 (original): The method as recited in claim 6, wherein the receiving slot is defined in the first printed circuit board.

Appl. No. 10/787,023 Amdt. Dated Feb. 3,2005

Reply to Office Action of November 3, 2004

Claim 8 (original): The method as recited in claim 7, wherein the first and the second quadrants of every tow orthogonally arranged printed circuit boards each have the electrical connector arranged therein, and each connector is mounted on the second printed circuit board.

Claim 9 (original): The method as recited in claim 8, wherein the connectors respectively arranged in the first and the second quadrants are mirror image with respect to the second printed circuit board.

Claims 10 (original): The method as recited in claim 8, wherein the third and the fourth quadrants of every two orthogonally arranged printed circuit boards each have the electrical connector arranged therein.

Claim 11 (original): The method as recited in claim 10, wherein the connectors respectively arranged in the third and the fourth quadrants are mirror image with respect to the second printed circuit board.

Claim 12 (original): The method as recited in claim 11, wherein the connectors respectively arranged in the first and the fourth quadrants are mirror image with respect to the first printed circuit board.

Claim 13 (currently amended): A method for configuring an electrical system adapted for mating with a complementary device, comprising the steps of:

providing a printed circuit board having a surface;

FEB-03-2005 20:38 FOXCONN 408 919 8353 P.06

Appl. No. 10/787,023 Amdt. Dated Feb. 3,2005 Reply to Office Action of November 3, 2004

providing a first group of conductive traces on the surface;

providing a second group of conductive traces on the surface and spaced from the first conductive traces;

mounting a first electrical connector on the first group of the conductive traces, the first electrical connector defining a mating face, the first electrical connector comprising first contacts moveably contacting with the first conductive pads; and

mounting a second electrical connector on the second group of the conductive traces, the second electrical connector defining a second mating face facing the first mating face, the second electrical connector comprising second contacts moveably contacting with the second conductive pads.

Claim 14 (currently amended): A method of making an interconnection system, comprising steps of:

providing a first set of parallel spaced printed circuit boards defining first front edge sections thereof, respectively;

providing a second set of parallel spaced printed circuit boards defining second front edge sections thereof, respectively; and

intersecting each of said first set of parallel spaced printed circuit boards with all of said second set of parallel spaced printed circuit boards, respectively, around the first front edge section of said each of the first set of parallel spaced printed circuit boards and the second front edge sections of said second set of parallel spaced printed circuit boards.; and

providing at least one electrical connector located in one of four quadrants

FEB-03-2005 20:38 F0XCONN 408 919 8353 P.07

Appl. No. 10/787,023 Amdt. Dated Feb. 3,2005

Reply to Office Action of November 3, 2004

derived from intersection by said each of said first set of parallel spaced printed

circuit boards and the corresponding one of said second set of parallel spaced

printed circuit board, and electrically connected to said each of said first set of

parallel spaced printed circuit boards and the corresponding one of said second set

of parallel spaced printed circuit boards, said connector extending in a longitudinal

direction with a plurality of juxtaposed contacts therein, and said longitudinal

direction being parallel to a center line defined by said four quadrants.

Claim 15 (cancelled)

Claim 16 (cancelled)

Claim 17 (new): The method as recited in claim 13, wherein the first and the

second electrical connectors each comprise contacts and an actuator adapted for

actuating end portion of the contacts to electrically contact with the printed circuit

board.

6